

FIG. 1 is a block diagram of a system 100. The system 100 includes a controller 104, a memory 126, and replaceable media 120. The controller 104 is connected to the memory 126. The memory 126 includes a first memory 128 and a second memory 124. The replaceable media 120 is connected to the memory 126.

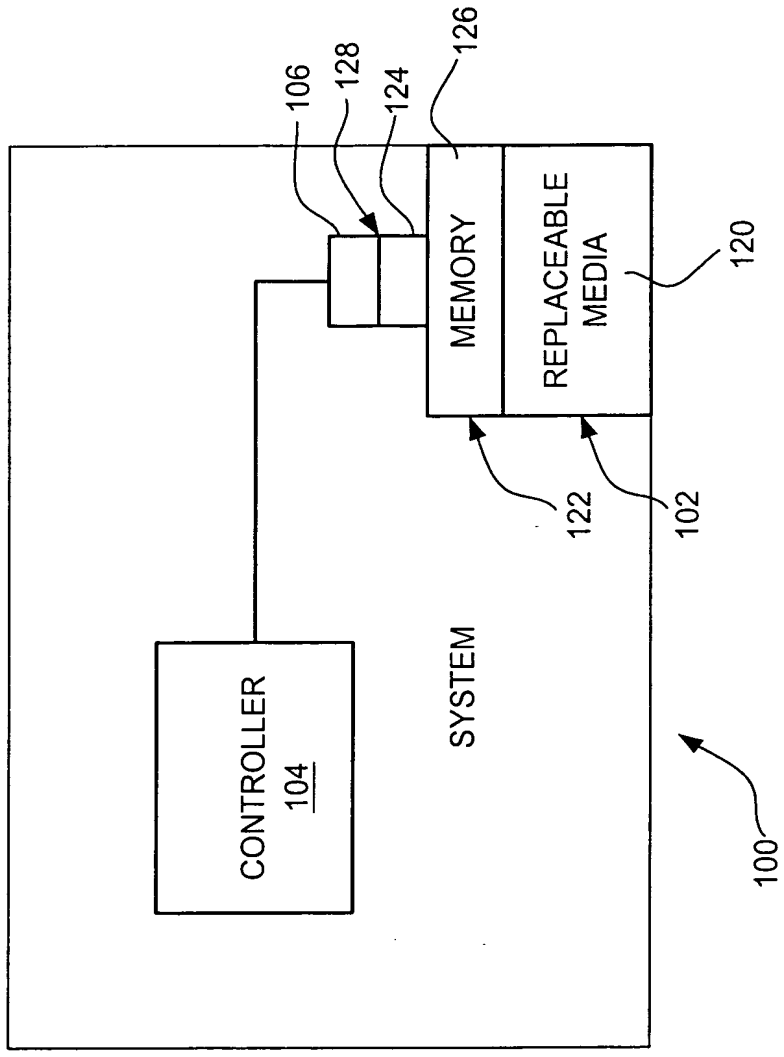


FIG. 1

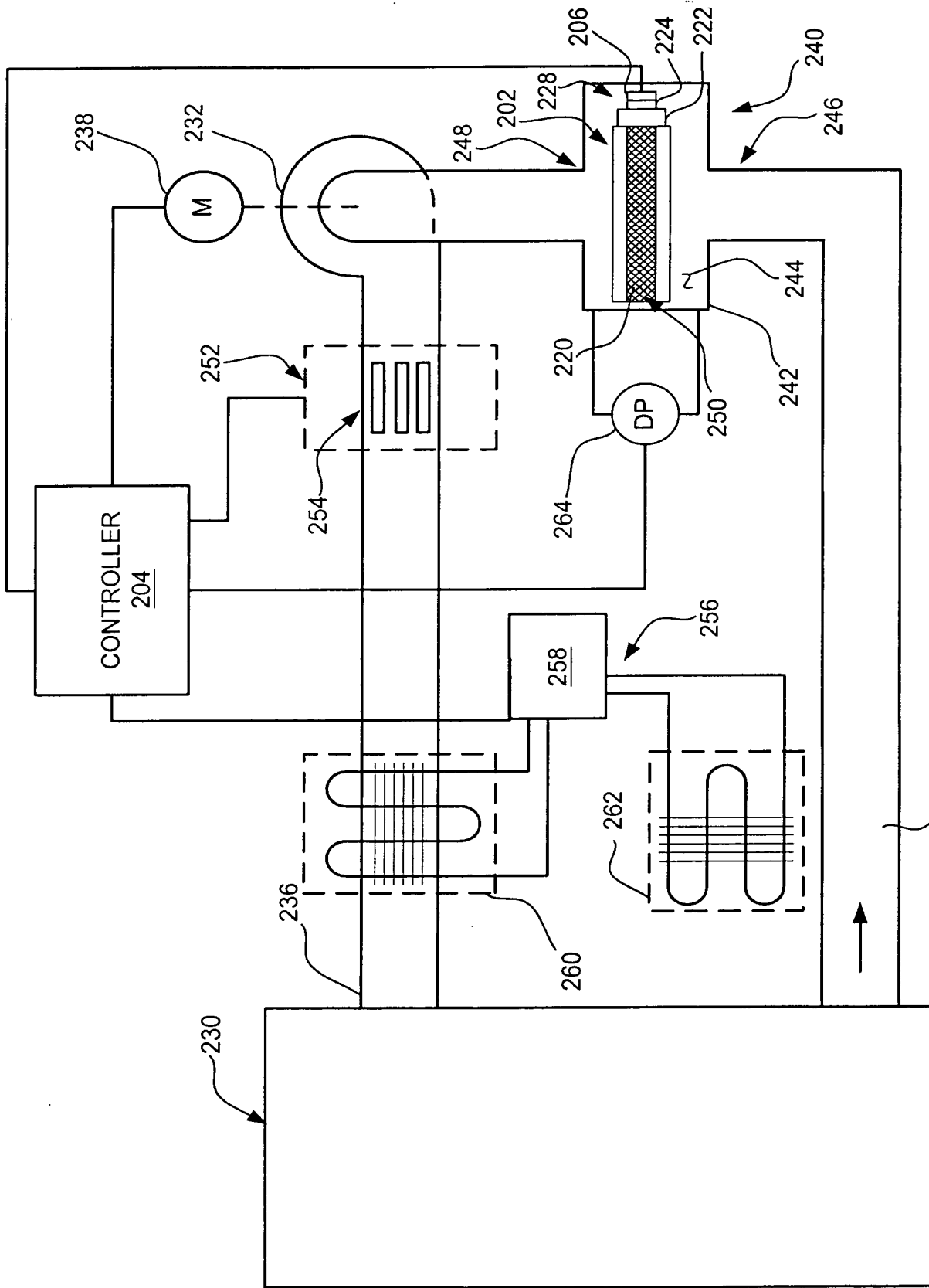


FIG. 2

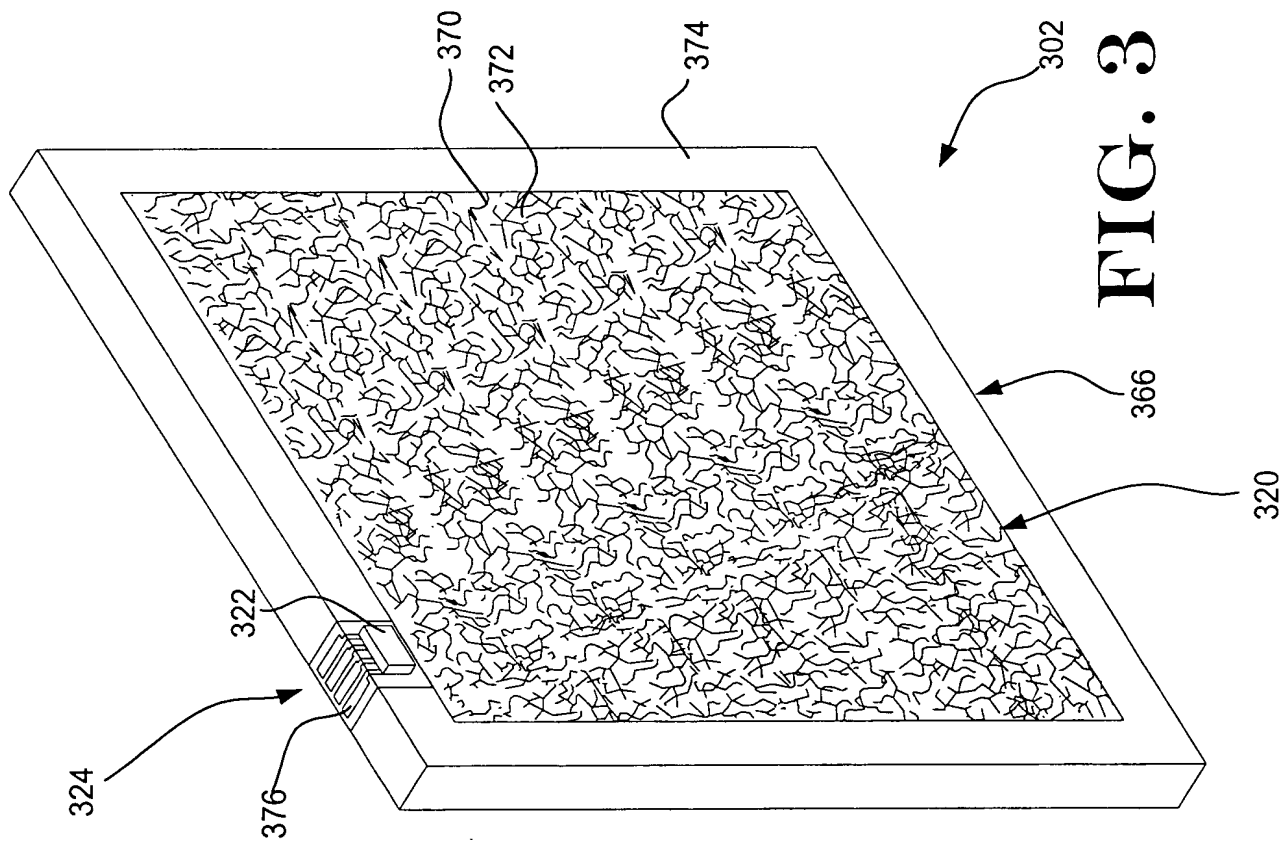


FIG. 3



